EPSON

Ultracompact Liquid crystal modules

September/1998





SEIKO EPSON CORPORATION

Vivid reproduction of brightness

EPSON TFT Ultracompact Liquid Crystal Modules

With polysilicon TFT technology, liquid crystal driver can be integrated

Ultracompact, high pixel density and high quality image

Natural color reproduction in normally white mode

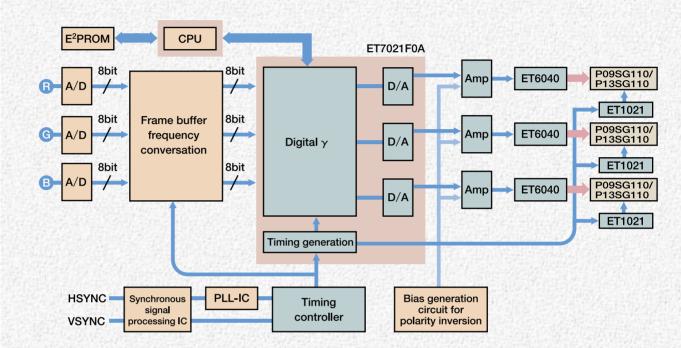
High contrast display provided by active matrix technology



Block diagram of the TFT drive circuit

P09SG110/P13SG110 0.9"/1.3" SVGA panel (Digital γ correction)

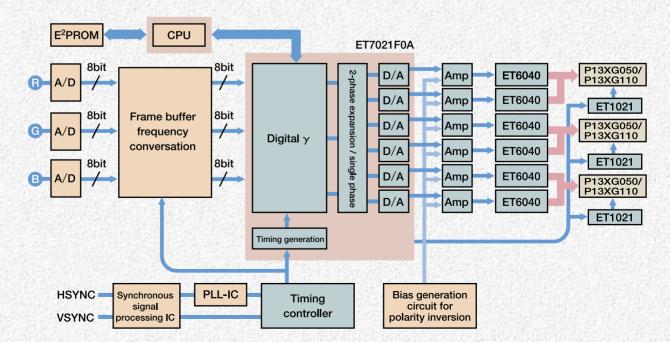
- •6-phase expansion using "analog sample and hold IC".
- Digital γ correction (8-bit input to 10-bit processing, lookup table system using E²PROM).
- •The video line offset need not be adjusted (6-phase expansion).
- •To input the video signal that exceeds 40 MHz, use the frame buffer to convert frequencies.



P13XG050/P13XG110

1.3" XGA panel (Digital γ correction)

- •12-phase expansion using "analog sample and hold IC" × 2.
- •Digital γ correction (8-bit input to 10-bit processing, lookup table system using E²PROM).
- •To input the video signal that exceeds 65 MHz, use the frame buffer to convert frequencies.

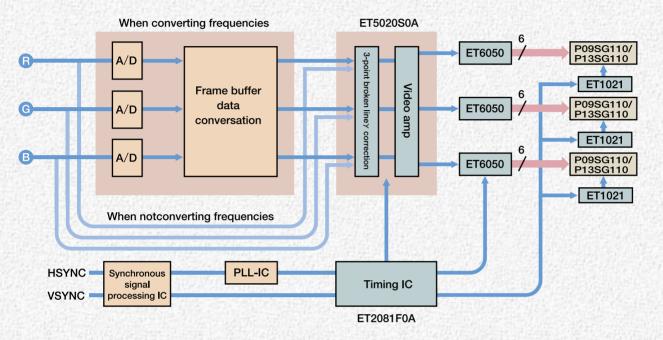




P09SG110/P13SG110

0.9"/1.3" SVGA panel (Analog γ correction)

- •A simple analog input system can be implemented.
- •6-phase expansion using "analog sample and hold IC".
- Analog γ correction
- •The video line offset need not be adjusted (6-phase expansion).
- •To input the video signal that exceeds 40 MHz, use the frame buffer to convert frequencies.

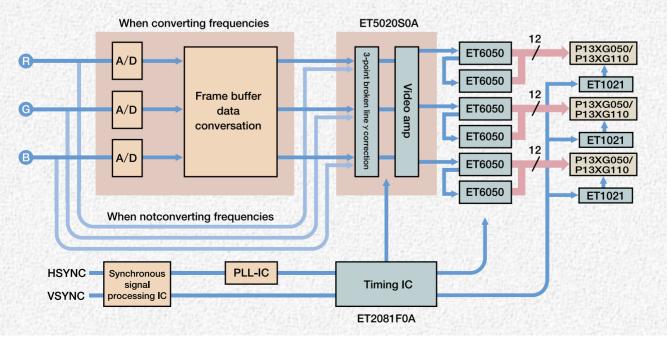


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P13XG050 / P13XG110

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EPSON TFT MODULE

		P09SG110	P13SG110	P13XG110	P13XG050
		0.9-inch	1.3-inch	1.3-inch	1.3-inch
		Dustproofed	Dustproofed	Dustproofed	Dustproofed
					Microlens included
		SVGA display	SVGA display	XGA display	XGA display
Screen size	Diagonal	1.9cm (0.9")	3.3cm (1.3")	3.3cm (1.3")	3.3cm (1.3")
	$\textbf{Width} \times \textbf{height}$	18.5×13.9	26.4×19.8	26.6×20.0	26.6×20.0
Number of dots	Horizontal \times vertical	804×604	800×600	1024×768	1024×768
External dimensions	$\textbf{Width} \times \textbf{height} \times \textbf{depth (mm)}$	$31.0\times32.0\times6.0$	$38.0\times42.0\times6.2$	$38.0 \times 42.0 \times 6.2$	$38.0\times42.0\times6.2$
Number of display colors		Monochrome	Monochrome	Monochrome	Monochrome
		analog	analog	analog	analog
Display mode		Normally white	Normally white	Normally white	Normally white
Contrastratio (room temperature)		Over 200:1	Over 200:1	Over 200:1	Over 200:1
Operating temperature (°C)		0 to 70	0 to 70	0 to 70	0 to 70
Storage temperature (°C)		-30 to 80	-30 to 80	-30 to 80	-30 to 80
Dot layout		Square lattice	Square lattice	Square lattice	Square lattice

^{*}These modules are custom made products, and made changes without notice.

Peripheral LSI Lineup

① Device type IC Lineup

	P09SG110 P13SG010		P13XG110 P13XG050		
	Digital γ correction	Analog γ correction	Digital γ correction	Analog γ correction	
Video processor IC	ET7021F0A	-	ET7021F0A	-	
RGB driver IC	-	ET5020S0A	-	ET5020S0A	
Timing control IC	ET7021F0A	ET2081F0A	ET7021F0A	ET2081F0A	
Sample/Hold IC	ET6040S0A	ET6050S0A	ET6040S0A	ET6050S0A	
Interface IC	ET1021F0A	ET1021F0A	ET1021F0A	ET1021F0A	
Block diagram	1	2	3	4	

②List of IC Specifications

	ET7021F0A	ET5020S0A	ET6040S0A	ET6050S0A	ET1021F0A
Package shape	QFP	QFP	QFP	QFP	QFP
Number of pins	176	52	80	64	80
Spacing (mm)	0.5	0.65	0.65	0.8	0.5
Ext. dimensions (mm)	24×24	10×10	14 × 14	14 × 14	12 × 12

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