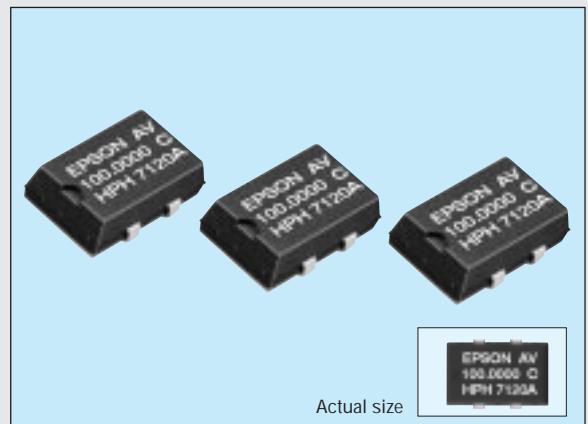


# PROGRAMMABLE HIGH-STABILITY HIGH-FREQUENCY CRYSTAL OSCILLATOR HG-8002JA series

- Wide frequency output by PLL technology.
- Low current consumption by output enable function (OE) or standby function (ST).
- Pin compatible with SG-615.
- Low current consumption due to use of C-MOS technology.
- Excellent shock resistance and environmental capability.



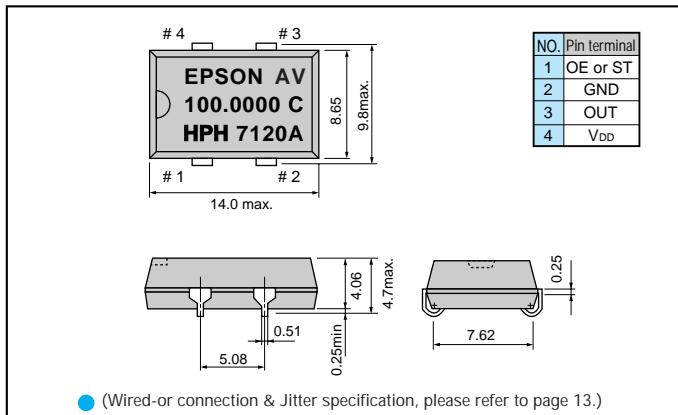
## ■ Specifications (characteristics)

Item	Symbol	PT/ST	PH/SH	PC/SC	Remarks
		Specifications			
Output frequency range	f <sub>0</sub>		1.0000 MHz to 125.0000 MHz		
Power source voltage	V <sub>DD</sub> -GND		-0.5V to +7.0V		
Operating voltage	V <sub>DD</sub>	5.0V±0.25V		3.3V ± 0.165V	
Temperature range	T <sub>STG</sub>		-55°C to +125°C		
	T <sub>OPR</sub>		-20°C to +70°C (-40°C to +85°C)		Refer to page 6."Frequency range"
Soldering condition	T <sub>SOL</sub>		Twice at under 260°C within 10 sec. or under 230°C within 3 min.		
Frequency stability	Δf/f <sub>0</sub>	AV: ±20ppm BV: ±25ppm CX: ±30ppm(-40°C to +85°C)			-20°C to +70°C
Current consumption	I <sub>op</sub>	45mA max.	28mA max.		No load condition, Max. frequency range
Output disable current	I <sub>OE</sub>	30mA max.	16mA max.		OE=GND(PT, PH, PC)
Standby current	I <sub>ST</sub>		50 μA max.		ST=GND(ST, SH, SC)
Duty	t <sub>w</sub> / t	— 40% to 60%	40% to 60%		C-MOS load: 1/2V <sub>DD</sub> level, Max. load condition TTL load: 1.4V level, Max. load condition
High output voltage	V <sub>OH</sub>		V <sub>DD</sub> -0.4V min.		I <sub>OH</sub> =-16mA(PT/ST, PH/SH), -8mA(PC/SC)
Low output voltage	V <sub>OL</sub>		0.4V max.		I <sub>OL</sub> = 16mA(PT/ST, PH/SH), 8mA(PC/SC)
Output load condition (fan out)	TTL	N	2TTL max.	—	
	C-MOS	C <sub>L</sub>	15pF max.		Max. frequency and max. operating voltage range
Output enable/disable input voltage	V <sub>IH</sub>		2.0V min.	0.7 × V <sub>DD</sub> min.	
	V <sub>IL</sub>		0.8V max.	0.2 × V <sub>DD</sub> max.	ST, OE terminal
Output rise time	t <sub>TLH</sub>	— 4ns max.		3ns max.	C-MOS load: 20%→80% V <sub>DD</sub> level TTL load: 0.4V→2.4V level
Output fall time	t <sub>THL</sub>	— 4ns max.		3ns max.	C-MOS load: 80%→20% V <sub>DD</sub> level TTL load: 2.4V→0.4V level
Oscillation start up time	t <sub>osc</sub>		10ms max.		Time at minimum operating voltage to be 0 sec.
Aging	f <sub>a</sub>		±2ppm/year max.		Ta= 25°C, V <sub>DD</sub> = 5.0V/3.3V(PC/SC)
Shock resistance	S.R.		±2ppm max.		Three drops on a hard board from 75 cm or excitation test with 3000G x 0.3ms x 1/2sine wave in 3 directions

Note: • Please contact us for inquiries about operating temperature(-40°C to +85°C), usable frequencies, duty and output load conditions.

## ■ External dimensions

(Unit: mm)



## ■ Recommended soldering pattern

(Unit: mm)

